



Applicant : Kusumoto et al. Art Unit : 2814
Serial No. : 09/903,339 Examiner :
Filed : July 10, 2001
Title : METHOD FOR PRODUCING INSULATED GATE THIN FILM
SEMICONDUCTOR DEVICE

#5/IDS
3/20/02
18

Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

This filing is being made before the receipt of a first Office action on the merits. No fee is required.

Documents AL-AO listed on the herewith Form PTO-1449 were cited in a Search Report dated October 9, 2001, issued in a foreign counterpart application not more than three months ago. To the best knowledge of the undersigned, this is the first citation of these items in any communication from a foreign patent office in a counterpart foreign application.

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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Examiner's initials in the left column per MPEP 609 are
earnestly solicited along with an early action on the merits.

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Respectfully submitted,

Date: 12/17/01



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